



Ph Free

RoHS Compliant

Features

- "Small size", 0.063cc (5×7×1.8mm)
- Using 1 chip HCMOS IC
- 3-state devices are available
- APR (Absolute Pull Range) up to $\pm 50 \times 10^{-6}$ or $\pm 100 \times 10^{-6}$

Applications

- Digital Switching System
- ATM SDH
- SONET
- xDSL

How to Order

KV7050L 25.0000 C 3 0 A 00
① ② ③ ④ ⑤ ⑥ ⑦

- ① Type (7.0×5.0mm SMD)
- ② Output Frequency
- ③ CMOS Output
- ④ Supply Voltage: 3 → 3.3V, 5 → 5V
- ⑤ Frequency Tolerance
F → $\pm 100 \times 10^{-6}$, G → $\pm 50 \times 10^{-6}$
- ⑥ Enable/ Symmetry 40 to 60%
- ⑦ Individual Specification

Specifications

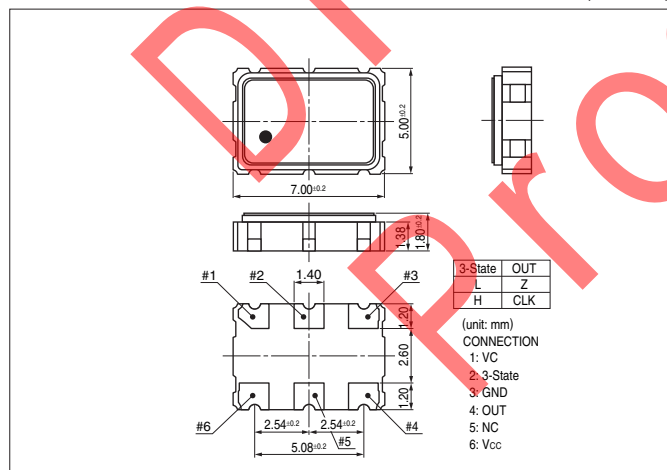
Item	Symbol	Conditions	Specification		Units
			KV7050	KV7050L	
Output Frequency Range	F _o		7.2 to 30		MHz
Storage Temperature Range	T _{stg}		-40 to +90		°C
Operating Temperature Range	T _{use}		-40 to +85		°C
Absolute Pull Range (APR)	APR	Included 20 years aging (at +25°C)	$\pm 50 / \pm 100^{*1}$ (VC=0.5 to 5.0V)	$\pm 50 / \pm 100^{*1}$ (VC=0.3 to 3.3V)	$\times 10^{-6}$
Max. Supply Voltage	—		-0.5 to 7		V
Supply Voltage	V _{cc}		5 ±10%	3.3 ±10%	V
Current Consumption	I _{cc}		15 max.		mA
Control Voltage	VC		2.5 ±2.5	1.65 ±1.65	V
Symmetry	SYM	50% V _{cc}	40 to 60		%
Rise Time/ Fall Time	tr/ tf		10 max.		nS
Low Level Output Voltage	V _{OL}		10% V _{cc} max.		V
High Level Output Voltage	V _{OH}		90% V _{cc} min.		V
Output Load	CL		15 ±10%		pF

*1 For 100×10^{-6} , 7.2MHz to 30MHz

• APR=Absolute Pull Range is the minimum guaranteed (voltage controlled) frequency shift ($\Delta f / f$ Nom)

Dimensions

(Unit: mm)



Recommended Land Pattern

(Unit: mm)

